

<b>Semester: 1</b>				
<b>Programme :</b> B.Sc.				
<b>Course :</b> COMPUTER FUNDAMENTALS AND DIGITAL DESIGN				
<b>Paper code:</b> B1CS230111T			<b>Credits: 4</b>	
<b>Hours/week :</b> Theory: 4				
<b>Category:</b> Core/MDC/SEC/VAC : Minor				
<b>Theory / Practical / Composite :</b> Theory				
<b>No of Modules :</b> 2				
<p><b>Course Overview:</b> This introductory course provides a dual-focus foundation in <b>computer systems</b> and <b>digital electronics</b>.</p> <p><b>Group A</b> covers the essentials of computing: computer architecture, generations, functional units, I/O and storage devices, and bus structures. Students learn problem-solving techniques using algorithms and flowcharts, explore software classifications, and gain practical knowledge of networking concepts—including LAN/MAN/WAN, Internet protocols, DNS, communication media, and services like email and video conferencing.</p> <p><b>Group B</b> builds the logical and mathematical groundwork for digital systems. Topics include number systems (Binary, Octal, Decimal, Hexadecimal), code representations, binary arithmetic, logic gates, Boolean algebra, and simplification techniques using Karnaugh Maps. Students apply this knowledge to design and analyze combinational circuits such as adders, comparators, multiplexers, decoders, and encoders.</p>				
<b>Course Outcome:</b>				
1. <b>Explain</b> the fundamental architecture of computer systems and <b>construct</b> algorithms and flowcharts for basic problem-solving.				
2. <b>Classify</b> software types and <b>analyze</b> network architectures, internet protocols, and communication services.				
3. <b>Convert</b> data across positional number systems (Binary, Octal, Decimal, Hex) and <b>perform</b> binary arithmetic operations.				
4. <b>Simplify</b> Boolean expressions using Boolean laws and Karnaugh Maps to <b>analyze</b> logic gate configurations.				
5. <b>Design</b> and <b>implement</b> combinational logic circuits such as Adders, Multiplexers, Encoders, and Decoders.				
<b>Prerequisites:</b>				
<ul style="list-style-type: none"> <li>• None</li> </ul>				
<b>SYLLABUS</b>				
UNIT/Module	CONTENT	HOURS or NUMBER OF CLASSES	CO Mapping	COGNITIVE LEVEL
<b>Group A</b>				
I.	<b>Introduction to Computer:</b> Different Generations, Functional Units, Basic I/O devices, Storage devices, Bus Structure	5	CO1	Understand, Apply (K2, K3)
II.	<b>Introduction to Problem Solving:</b> Concept of Data and Information, Basic	8	CO1	Understand, Apply

	problem-solving using Flowchart and Algorithm			(K2, K3)
III.	<b>Software:</b> Types and Brief Ideas about Each of the Types	5	CO2	Understand, Analyze (K2, K4)
IV.	<b>Introduction to Networking:</b> Advantages of Networking; Basic Features, LAN, MAN and WAN; characteristic features. Intranet and Internet; Servers and Clients; Ports; Domain Name System (DNS); WWW, Browsers. Guided and Unguided media. Modem; E-mail, Voice and Video Conferencing.	8	CO2	Understand, Analyze (K2, K4)
<b>Group B</b>				
V.	<b>Number Systems and Codes:</b> Weighted and Non-Weighted Codes, Positional Number Systems like Binary, Octal, Decimal and Hexadecimal, Conversion of one number system to another, BCD.	4	CO3	Apply (K3)
VI.	<b>Binary Arithmetic:</b> Addition and Subtraction	3	CO3	Apply (K3)
VII.	<b>Logic Gates</b> AND, OR, NOT, NAND, NOR, XOR.	4	CO4	Analyze (K4)
VIII.	Boolean expression, Laws of Boolean Algebra, Simplification, Design of simple logic circuits, Sum of Product, Product of Sum, Simplification using Karnaugh Map, Applications	6	CO4	Analyze (K4)
IX.	<b>Combinational circuits</b> – Adder/ Subtractor, Comparator circuit	5	CO5	Create (K6)
X.	<b>Other Combinational Circuits</b> – Multiplexer, Decoder, Encoder.	4	CO5	Create (K6)
<b>Text Books</b>				
1. Computer Fundamental- P.K Sinha, BPB Publications.				
2. Digital Logic and Computer Design, M Morris Mano, Pearson education India.				
3. Fundamentals of Computers, V. Rajaraman, PHI.				
4. Data Communications and Networking, B Forouzan, Mc Graw Hill				
<b>Evaluation</b>				
Theory CIA: 25 Attendance: 5 Semester Exam: 70				
<b>Paper Structure for Theory Semester Exam Module:</b> Answer 5 out of 7 of 7 marks each in Group A Answer 5 out of 7 of 7 marks each in Group B				

### Course outcomes (COs) and Cognitive Level Mapping

COs	CO Description	Cognitive levels
CO1	<b>Explain</b> the fundamental architecture of computer systems and <b>construct</b> algorithms and flowcharts for basic problem-solving.	Understand, Apply (K2, K3)
CO2	<b>Classify</b> software types and <b>analyze</b> network architectures, internet protocols, and communication services.	Understand, Analyze (K2, K4)
CO3	<b>Convert</b> data across positional number systems (Binary, Octal, Decimal, Hex) and <b>perform</b> binary arithmetic operations.	Apply (K3)
CO4	<b>Simplify</b> Boolean expressions using Boolean laws and Karnaugh Maps to <b>analyze</b> logic gate configurations.	Analyze (K4)
CO5	<b>Design</b> and <b>implement</b> combinational logic circuits such as Adders, Multiplexers, Encoders, and Decoders.	Create (K6)