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| **Paper Code:** HCSDS5012P | **Microprocessor****(Theory)** | **Marks: 60** |
| **Sl. No.** | **Topic** | **No. of Periods** |
| 1 | Microprocessor architecture: Internal architecture,  | 8 |
| 2 | System bus architecture, memory and I/O interfaces. | 7 |
| 3 | Microprocessor programming: Register Organization | 5 |
| 4 | Instruction formats, assembly language programming. | 10 |
| 5 | Interfacing: Memory address decoding, cache memory and cache controllers, I/O interface, keyboard, display, timer,  | 12 |
| 6 | Interrupt controller, DMA controller, video controllers, communication interfaces. | 10 |
| **Total** | **52** |
| **Books and References:**1. Barry B. Brey : The Intel Microprocessors : Architecture, Programming and

Interfacing. Pearson Education, Sixth Edition,2009.1. Walter A Triebel, Avtar Singh; The 8088 and 8086 Microprocessors Programming,

Interfacing, Software, Hardware, and Applications. PHI, Fourth Edition 2005.1. Microprocessor Architecture, programming and application with the 8085 – Ramesh S. Gaonkar, 4th Edition, Penram International Publishing.
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| **Paper Code: DSE51AP** | **Microprocessor****(Practical)** | **Marks: 40** |